

DE2705100L Silicon epitaxial planar type

For ESD protection DE2S051 in SSSMini2 type package

- Features
- High ESD
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)
- Marking Symbol: CC
- Packaging

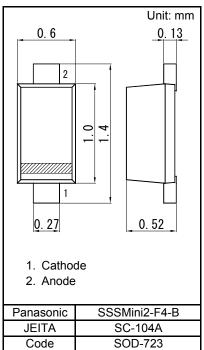
Embossed type (Thermo-compression sealing) 10 000 pcs / reel (standard)

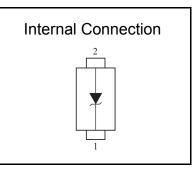
■ Absolute Maximum Ratings Ta = 25 °C

Falameter	Symbol	Rating	Unit
Total power dissipation ^{*1}	PT	120	mW
Electrostatic discharge *2	ESD	±30	kV
Junction temperature	Tj	150	°C
Operating ambient temperature	Topr	-40 to +85	°C
Storage temperature	Tsta	-55 to +150	3°

Note) *1: Mounted on glass epoxy print board. (45 mm x 45 mm x 1 mm) Solder in (0.4 mm x 0.3 mm)

*2: Test method:IEC61000_4_2(C = 150 pF,R = 330 Ω, Contact discharge:10 times)





Electrical Characteristics	$1a = 25 \degree C \pm 3 \degree C$			
Parameter	S	Symbol	Condi	

_	Parameter	Symbol	Conditions	Min	Тур	Max	Unit
	Zener voltage *1,*2	VZ	IZ = 1 mA	4.85		5.36	V
	Reverse current	IR	VR = 2 V			1.0	μA
	Terminal capacitance	Ct	VR = 0V, f = 1 MHz		75		pF
	Temperature coefficient of zener voltage *3	SZ	IZ = 1 mA		0.6		mV/°C

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Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 Measuring methods for Diodes.

2. *1: The temperature must be controlled 25°C for VZ mesurement.

VZ value measured at other temperature must be adjusted to VZ (25°C)

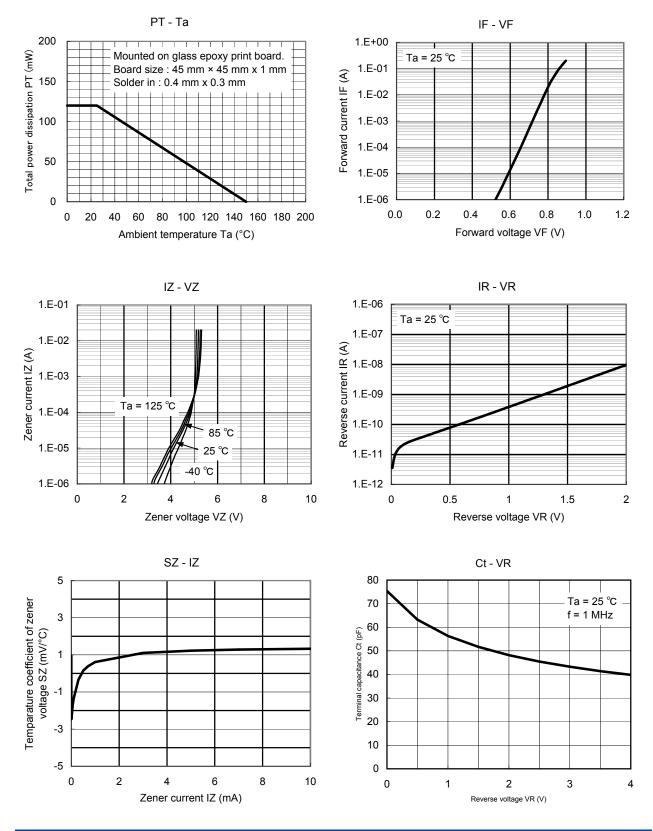
*2: VZ guaranted 20 ms after current flow.

*3: Tj = 25°C to 150°C





Technical Data (reference)



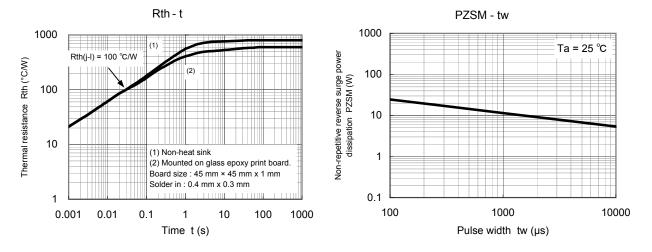
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Established : 2012-03-14 Revised : 2013-11-01





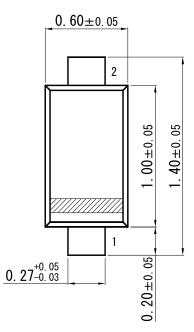
<u> Technical Data (reference)</u>

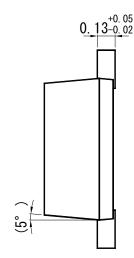


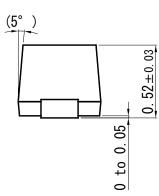


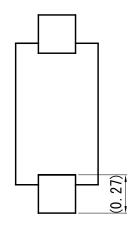
Unit: mm

SSSMini2-F4-B

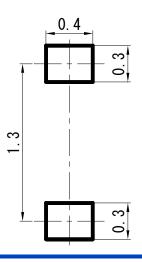








Land Pattern (Reference) (Unit: mm)



Established : 2012-03-14 Revised : 2013-11-01

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