Nch 45V 2A Middle Power MOSFET

V_{DSS}	45V
R _{DS(on)} (Max.)	180mΩ
I _D	±2.0A
P_D	1.0W

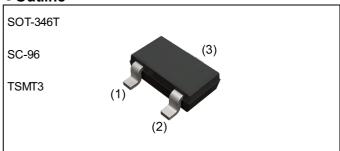
●Features

- 1) Low on-resistance
- 2) Space saving small surface mount package (TSMT3)
- 3) Low voltage drive(2.5V drive)

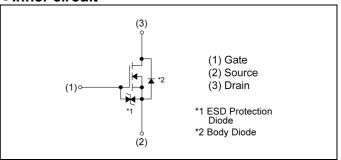
● Application

Switching

Outline



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Basic ordering unit (pcs)	3000
	Taping code	TL
	Marking	QF

ullet Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	45	V
Continuous drain current	I _D	±2.0	А
Pulsed drain current	I _{DP} *1	±8	А
Gate - Source voltage	V_{GSS}	±12	V
Down discipation	P _D *2	1.0	W
Power dissipation	P _D *3	0.76	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Dorameter	Symbol	Values			Lloit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres innetion, embient	R _{thJA} *2	-	1	125	°C/W
Thermal resistance, junction - ambient	R _{thJA} *3	-	1	165	°C/W

● Electrical characteristics (T_a = 25°C)

Parameter	Symbol Conditions		Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	45	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	46.8	-	mV/°C
Zero gate voltage drain current	I _{DSS}	V _{DS} = 45V, V _{GS} = 0V	1	-	1	μA
Gate - Source leakage current	I _{GSS}	$V_{GS} = 12V, V_{DS} = 0V$	1	-	10	μA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = 10V, I_{D} = 1mA$	0.5	-	1.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	-3.9	-	mV/°C
		V _{GS} = 4.5V, I _D = 2.0A	-	130	180	
Static drain - source on - state resistance	R _{DS(on)} *4	$V_{GS} = 4.0V, I_D = 2.0A$	-	135	190	mΩ
		$V_{GS} = 2.5V, I_D = 2.0A$	1	180	250	
Gate resistance	R_{G}	f = 1MHz, open drain	1	9.5	1	Ω
Forward Transfer Admittance	Y _{fs} *4	$V_{DS} = 10V, I_D = 2.0A$	1.5	-	-	S

^{*1} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*2} Mounted on a ceramic board (30x30x0.8mm)

^{*3} Mounted on a FR4 (25x25x0.8mm)

^{*4} Pulsed

●Electrical characteristics (T_a = 25°C)

Davamatav	Current ed	Conditions	Values			Unit	
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Ufill	
Input capacitance	C _{iss}	V _{GS} = 0V	-	200	-		
Output capacitance	C _{oss}	V _{DS} = 10V	-	45	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	25	1		
Turn - on delay time	t _{d(on)} *4	$V_{DD} \simeq 25V, V_{GS} = 4.5V$	-	11	1		
Rise time	t _r *4	I _D = 1.0A	-	16	-	no	
Turn - off delay time	t _{d(off)} *4	$R_L \simeq 25\Omega$	-	21	1	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	11	-		

● Gate charge characteristics (T_a = 25°C)

3 · · · · · · · · · · · · · · · · · · ·						
Parameter	Symbol Conditions	Canditions	Values			l lait
		Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Qg*4	V _{DD} ≃ 25V,	-	2.9	4.1	
Gate - Source charge	Q _{gs} *4	I _D = 2.0A, V _{GS} = 4.5V	-	0.7	-	nC
Gate - Drain charge	Q _{gd} *4	$V_{GS} = 4.5V$	-	0.9	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
raianetei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous forward current	I _S	T - 25°C	-	-	0.8	Α
Pulse forward current	I _{SP} *1	T _a = 25°C	-	-	8	Α
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = 0.8A	-	-	1.2	V

Fig.1 Power Dissipation Derating Curve

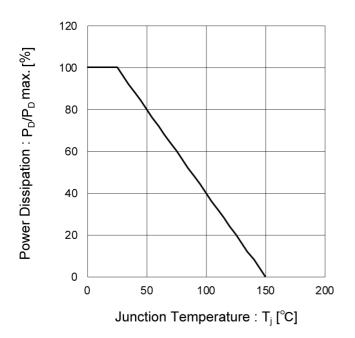
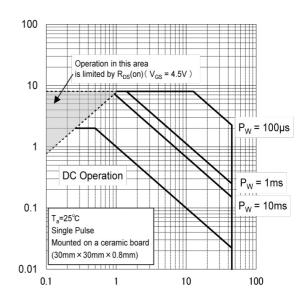


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

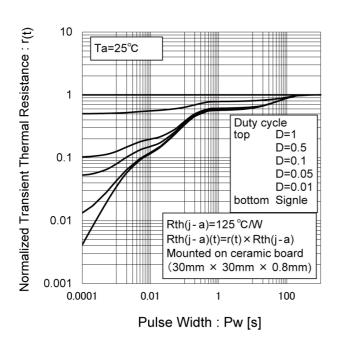


Fig.4 Single Pulse Maximum Power dissipation

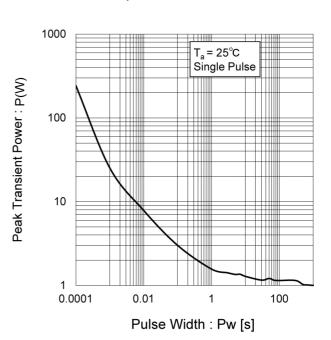
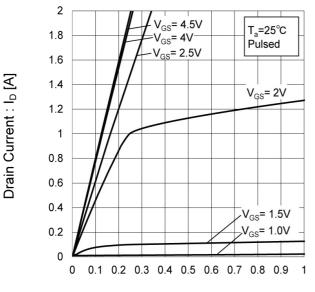
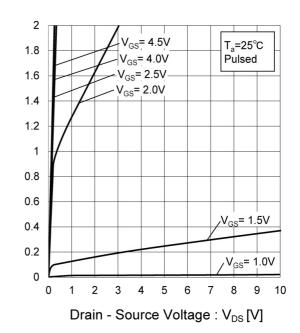


Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage : V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Fig.7 Breakdown Voltage vs.

Junction Temperature

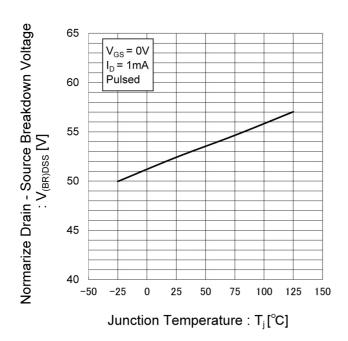
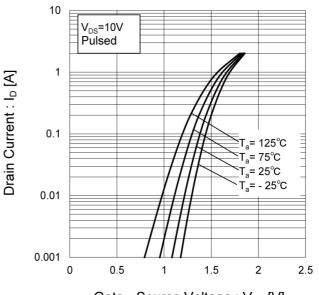


Fig.8 Typical Transfer Characteristics



Gate - Source Voltage : $V_{GS}[V]$

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

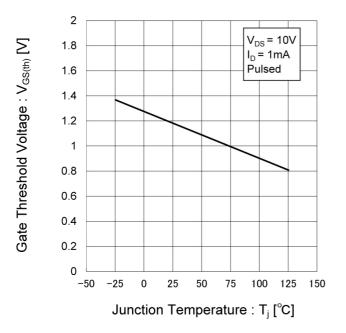


Fig.10 Forward Transfer Admittance vs.
Drain Current

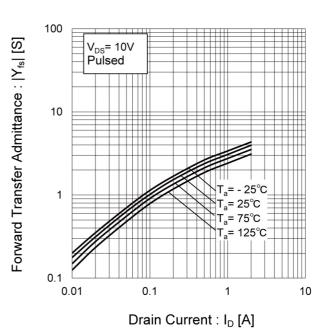


Fig.11 Drain Current Derating Curve

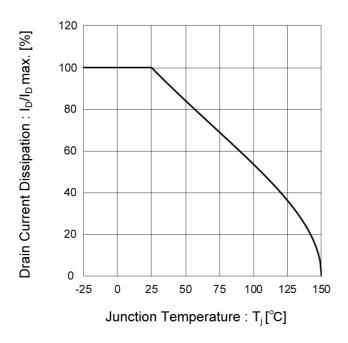
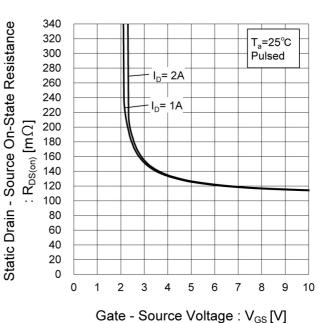


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

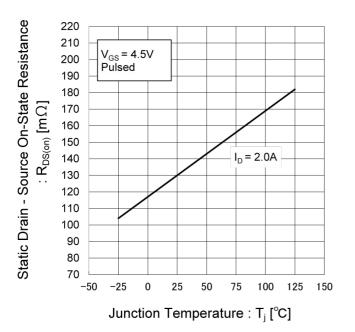


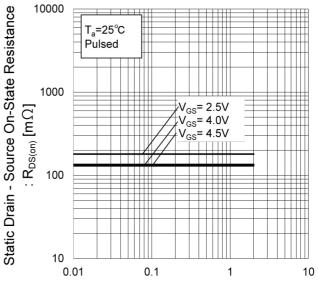
RTR020N05

• Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)





Drain Current : I_D [A]



Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

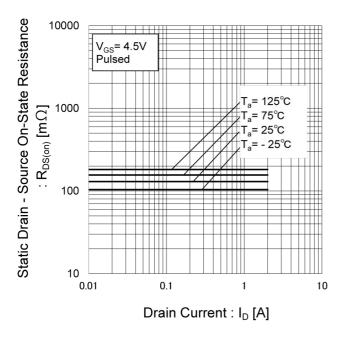


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

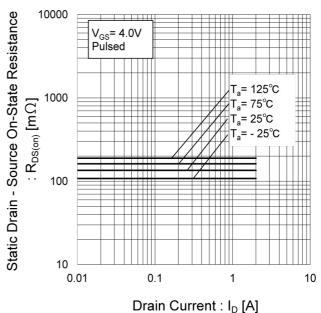


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)

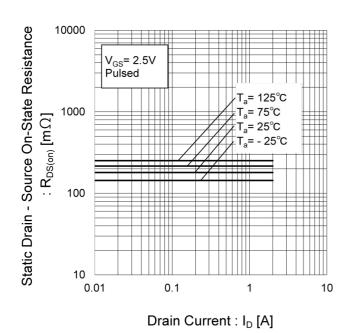


Fig.18 Typical Capacitance vs.

Drain - Source Voltage

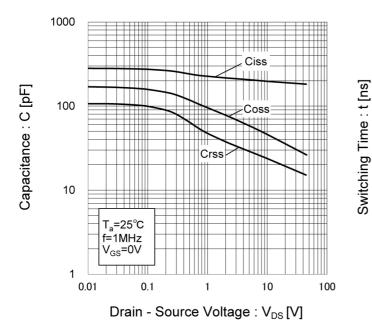


Fig.19 Switching Characteristics

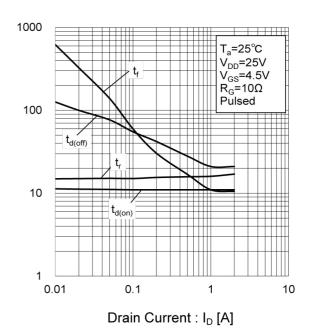
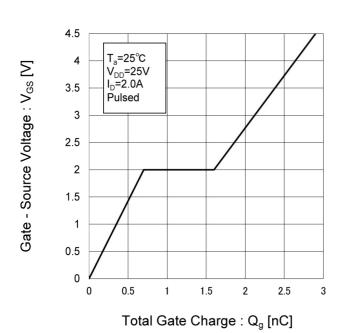
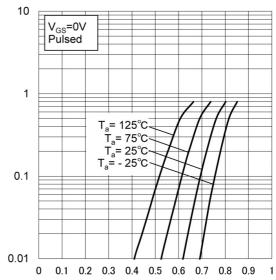


Fig.20 Dynamic Input Characteristics



Source Drain Voltage

Fig.21 Source Current vs.



Source Current : Is [A]

Measurement circuits

Fig. 1-1 SWITCHING TIME MEASUREMENT CIRCUIT

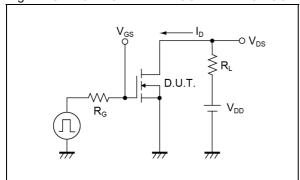


Fig. 2-1 GATE CHARGE MEASUREMENT CIRCUIT

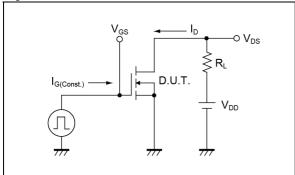


Fig. 1-2 SWITCHING WAVEFORMS

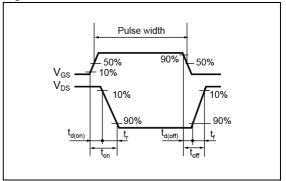
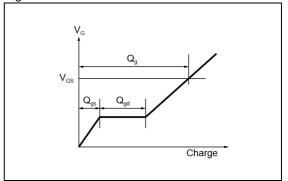
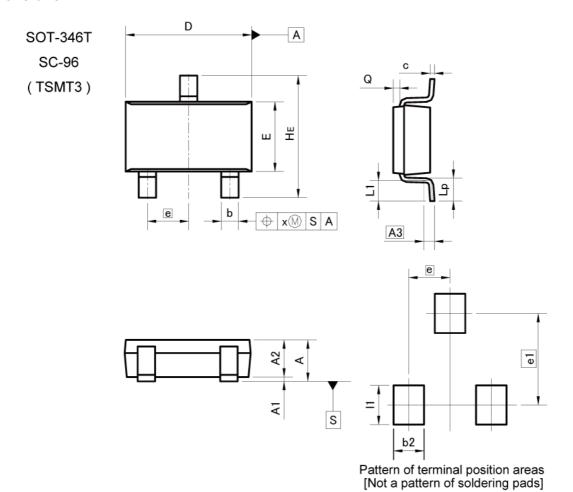


Fig. 2-2 GATE CHARGE WAVEFORM



Dimensions



DIM	MILIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	-	1.00	_	0.039
A1	0.00	0.10	0.000	0.004
A2	0.75	0.95	0.030	0.037
A3	0.:	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.10	0.26	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.95		0.0	37
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.05	0.25	0.002	0.010
×	- -2	0.20	==1	0.008

DIM	MILIMI	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
b2		0.70	-	0.028	
e1	2.10		0.0	83	
11	5 0	0.90	-	0.035	

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCIII
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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